

- Título de publicación de puesto
CMOS Digital Design Engineer
- Descripción de puesto
Teledyne e2v *Professional Imaging* is an international leader in high performance, ultra-high resolution, and ultra-high speed image sensors and systems with approximately 220 employees globally. Acquired by Teledyne Technologies in 2017, Teledyne e2v *Professional Imaging* designs, develops, manufactures, and markets CMOS image sensors, modules, and imaging solutions for a variety of applications including machine vision and automation, medical and life sciences, logistics and robotics, and the environment, food and recycling. From 1D, 2D and 3D sensing to multi-spectral imaging, ultra-low noise, high sensitivity sensors rivalling EMCCDs, the range of our capabilities makes us a preferred innovation partner amongst our customers.

Our unique approach involves listening to the market and application challenges of our customers and partnering with them to provide the most leading-edge standard, semi-custom, and fully custom imaging solutions.

Teledyne e2v *Professional Imaging* sensors, systems, and subsystems are designed, manufactured, tested and characterized in Grenoble (France) and Seville (Spain) and are supplied to leading customers, worldwide.

Working with us means engaging with a very talented and international team in the challenging world of high technology imaging. Join us to brighten your future!

At Teledyne, we believe that Equality, Diversity and Inclusion mean Opportunity – the opportunity to cherish and celebrate the value of diversity, to bring our full and authentic selves to work and to feel fully involved and respected – and we are committed to this journey. Occupational health and safety and environmental sustainability are also an integral part of our business strategy, and we believe in implementing environmental, health and safety improvements to continually enhance the working environment for staff, visitors and contractors.

This role is suited for a CMOS Digital Design Engineer looking for a new challenge and the opportunity to work within an experienced team, with analog and digital design embedded to achieve the best solution for standard and custom image sensors.

The candidate should have the potential to work with CMOS Image Sensors in several technologies and architectures, and should be able to work within a team of digital and analog designers, both internal and external subcontractors.

Personal development opportunities will be available throughout our global international organisation for high performers.

Your personal values must be in line with ours: Agility, Integrity, Excellence throughout and Customers first.

MAIN ACCOUNTABILITIES

Digital Design

- Responsible for digital design of any sub-block or digital top within an image sensor, from specifications to synthesis. Including constraints definition, testbench and functional verification.
- Work to ensure that designs are right first time or that only a metal fix is required.
- Ensure that newly designed circuits have been designed for ease of testability.

Innovation

- Protect our intellectual property.
- To contribute to the design of new technology building blocks to ensure that T-e2v can meet the needs of professional image customers in the future.

Key Internal interfaces:

- Technical Design Leader
- Digital Design Team
- Analog & Mixed Signal Design Team

Mobility/travel: <10% of the working time

Qualifications

- At least a Bachelor's Degree in Robotics/Electronics/Telecommunications Engineering or Physics, electronics specialty
- Knowledge of Hardware languages: Verilog, SystemVerilog/UVM and/or VHDL
- Some knowledge of CMOS digital design and flow
- English: good level written and spoken

Essential Competencies

Able to:

- Present justification of design options and choice made.
- Write technical documentation for internal use.

Non-technical skills:

- Adaptability with strong problems solving skill
- Good team working, communication, presentation, and interpersonal skills
- Team player spirit.
- Proactive approach

Desired Experience/Competencies

- Knowledge of
- Experience in the design of image CMOS sensors.
- Cadence EDA tool knowledge: Xcelium, Genus, Innovus, Modus, Tempus, PVS.
- Knowledge of script languages: TCL, Bash, Python, Perl
- Knowledge of device physics and CMOS technology and their interaction with VLSI circuits.