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- STMicroelectronics is a world leader in providing the semiconductor solutions that make a positive contribution to people's lives, today and into the future.
- Among the world's largest semiconductor companies
- A leading Integrated Device Manufacturer delivering solutions that are key to Smart Driving and the Internet of Things
- A leading technology innovator: ~7,800 people working in R&D and product design, ~18,500 patents, ~ 9,600 patent families and ~ 590 new patent filings in 2019
- An unwavering commitment to sustainability
- Corporate Headquarters: Geneva, Switzerland
- President and CEO: Jean-Marc Chery
- 2019 revenues: \$9.56 billion
- ~46,000 employees worldwide
- 80 sales & marketing offices in 35 countries
- More than 100,000 customers worldwide
- 11 main manufacturing sites
- Public since 1994: shares traded on the New York Stock Exchange (NYSE: STM), Euronext Paris, and Borsa Italiana
- Created as SGS-THOMSON Microelectronics in June 1987, from merger of SGS Microelettronica (Italy) and Thomson Semiconducteurs (France)
- Renamed STMicroelectronics in May 1998



<p>We are looking for our Crolles site:</p>	<p>INTERNSHIP - "EVALUATION OF A CALIBRATION METHOD ON SILICON FOR IMPROVING THE EXTRACTION OF RF PASSIVE UP TO 220GHZ"</p>
<p>Description</p>	<p>It is more and more crucial to be able to demonstrate the RF performances of the latest technologies dedicated to 5G frequencies.</p> <p>For this, we must study new RF / mmW measurement methods allowing a precise extraction of the performances of RF passive devices such as inductance, transmission line and capacitance...</p> <p>A preliminary study has already been carried out in collaboration with the IMS laboratory. Test structures have been integrated in BiCMOS55 technology. An experiment plan has been designed with on-wafer calibration structures for TRL (THRU-REFLECT-LINES) calibration.</p> <p>Mission:</p> <p>The STMicroelectronics contract student will be based at the IMS laboratory in Bordeaux in order to benefit from the experience of designing test structures and to have access to a measurement</p>

	<p>setup in the 140GHz-220GHz frequency range.</p> <p>Main tasks:</p> <ul style="list-style-type: none"> - Electromagnetic (EM) simulations of test structures with HFSS or Momentum (0-220 GHz) - Participate in on-wafer RF characterization, including the calibration methodology (0-220 GHz) - Perform a comparison of measurements / EM simulations on RF passive (0-220 GHz) - Analyze the potential of TRL calibration on silicon compared to standard industrial methods (ISS Cascade) in the range from 0 to 110 GHz.
Salary	1200€ gross salary per month
Level of study required	Master diploma
Skills	<ul style="list-style-type: none"> - Mandatory skills: Basis of design and measurement in radio frequency. Ideally concept of electromagnetic simulation and silicon technologies.
Référence	<p>D2019R10-2006</p> <p>romain.debroucke@st.com cedric.durand@st.com marina.deng@ims-bordeaux.fr</p>

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